

Docket No.: 60188-084

UTILITY PATENT APPLICATION
UNDER 37 CFR 1.53(b)

Box PATENT APPLICATION

Commissioner for Patents

Washington, DC 20231

Sir:

Transmitted herewith for filing is the patent application of:

INVENTOR: Shigeki FURUYA, Hisaki WATANABE, Atsushi MOTOTANI
FOR: CMOS BASIC CELL AND METHOD FOR FABRICATING
SEMICONDUCTOR INTEGRATED CIRCUIT USING THE SAME

Enclosed are:

- ☒ 62 pages of specification, claims, abstract.
- ☒ Declaration and Power of Attorney.
- ☒ Priority Claimed.
- ☒ Certified copy of Japanese Patent Application No. 2000-286668; and Japanese Patent Application No. 2001-035267
- ☒ 54 sheets of formal drawing.
- ☒ An assignment of the invention to MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD.
and the assignment recordation fee.
- ☒ An associate power of attorney.
- ☐ A verified statement to establish small entity status under 37 CFR 1.9 and 37 CFR 1.27.
- ☒ Information Disclosure Statement, Form PTO-1449 and reference.
- ☒ Return Receipt Postcard
- ☐

The filing fee has been calculated as shown below:

	NO. OF CLAIMS		EXTRA CLAIMS	RATE	AMOUNT
Total Claims	66	-20	46	\$18.00	\$828.00
Independent Claims	9	-3	6	\$80.00	\$480.00
Multiple Dependent Claim(s)					\$270.00
Basic Fee					\$710.00
Total of Above Calculations					\$2,288.00
Less ½ for Small Entity					\$0.00
Assignment & Recording Fee					\$40.00
Total Fee					\$2,328.00



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PATENT TRADEMARK OFFICE



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